

FIG. 1

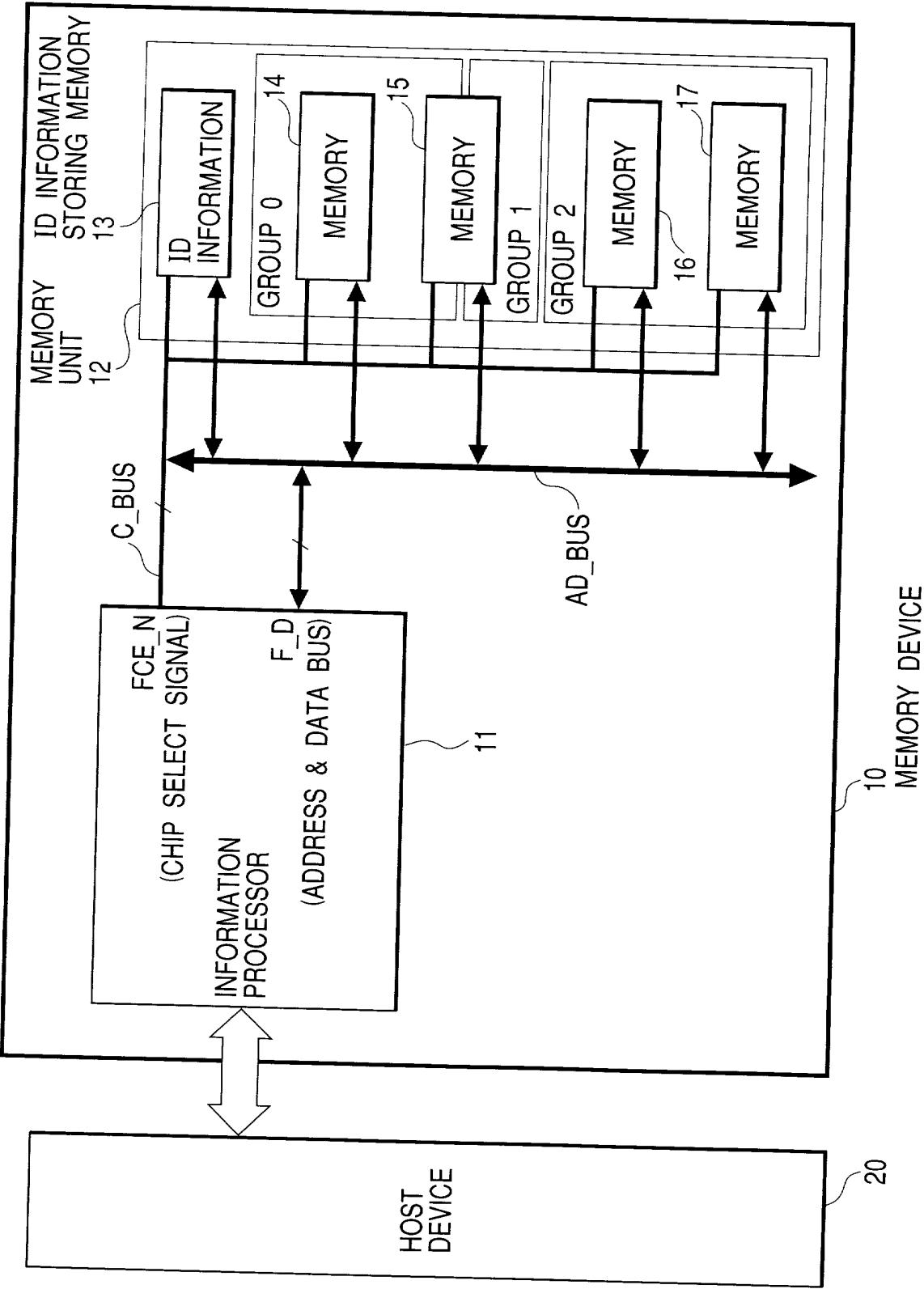
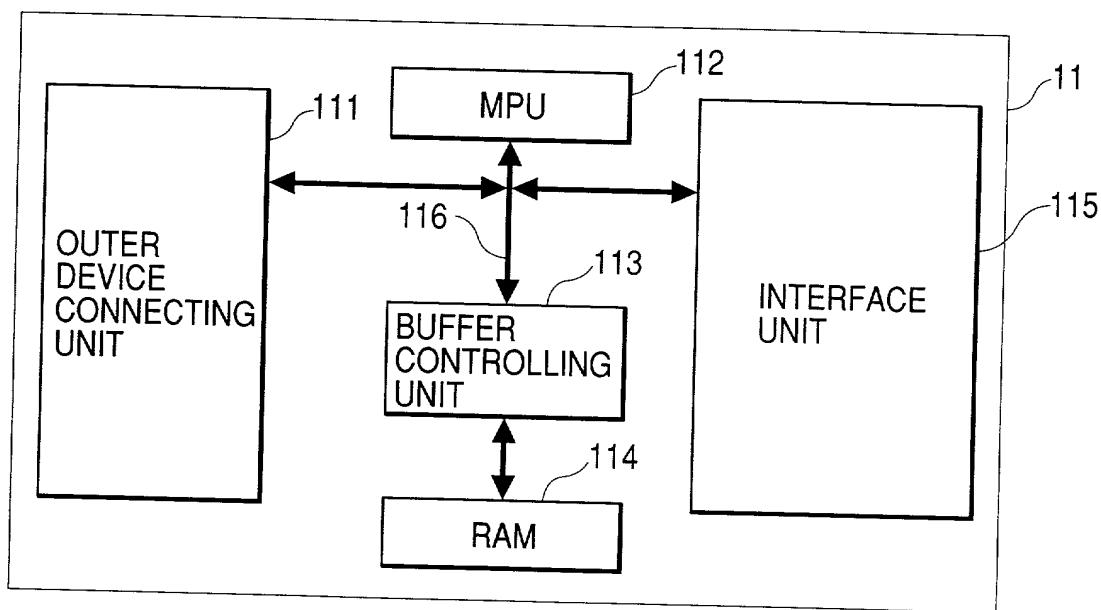
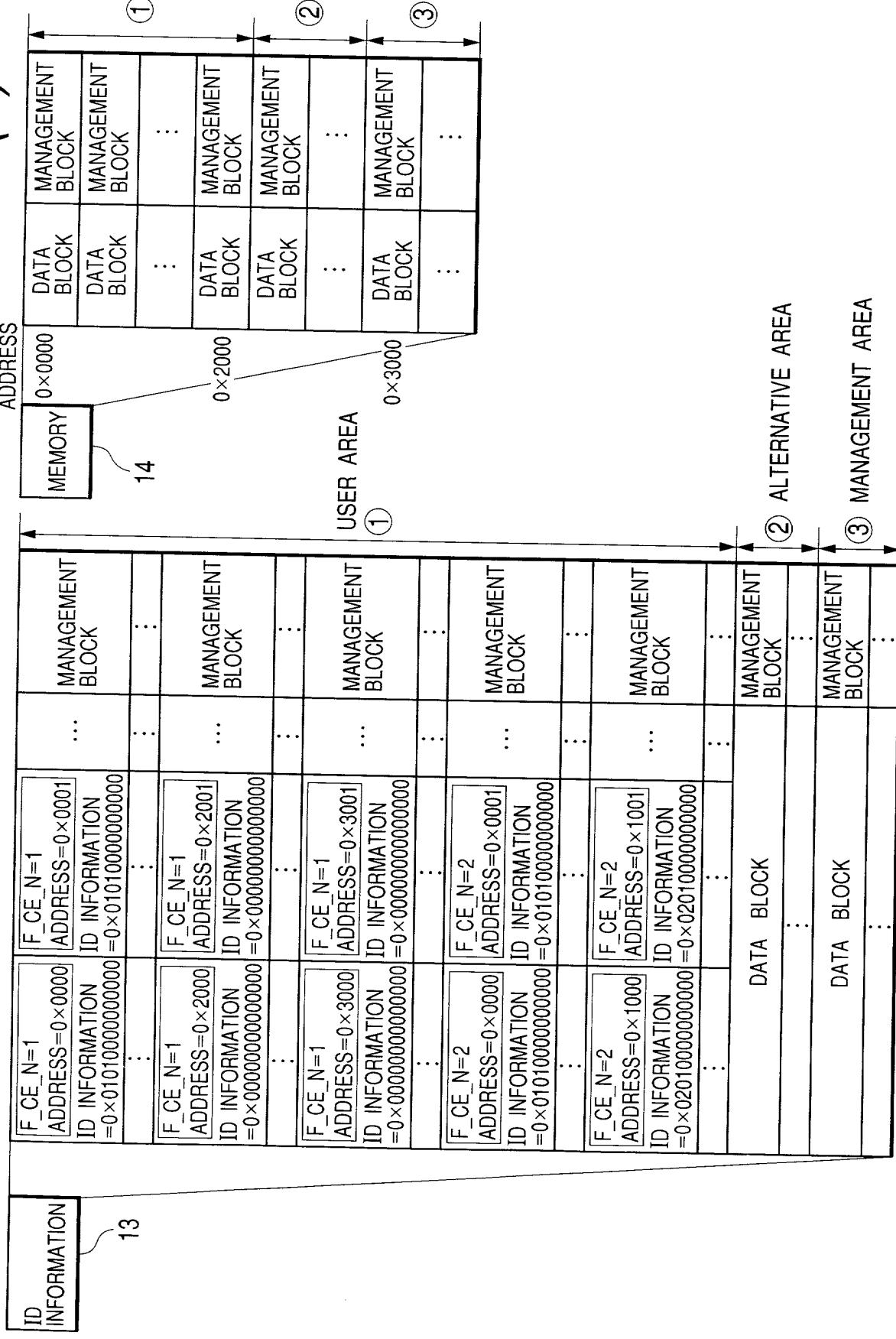


FIG. 2



**FIG. 3(A)**



**FIG. 3(B)**

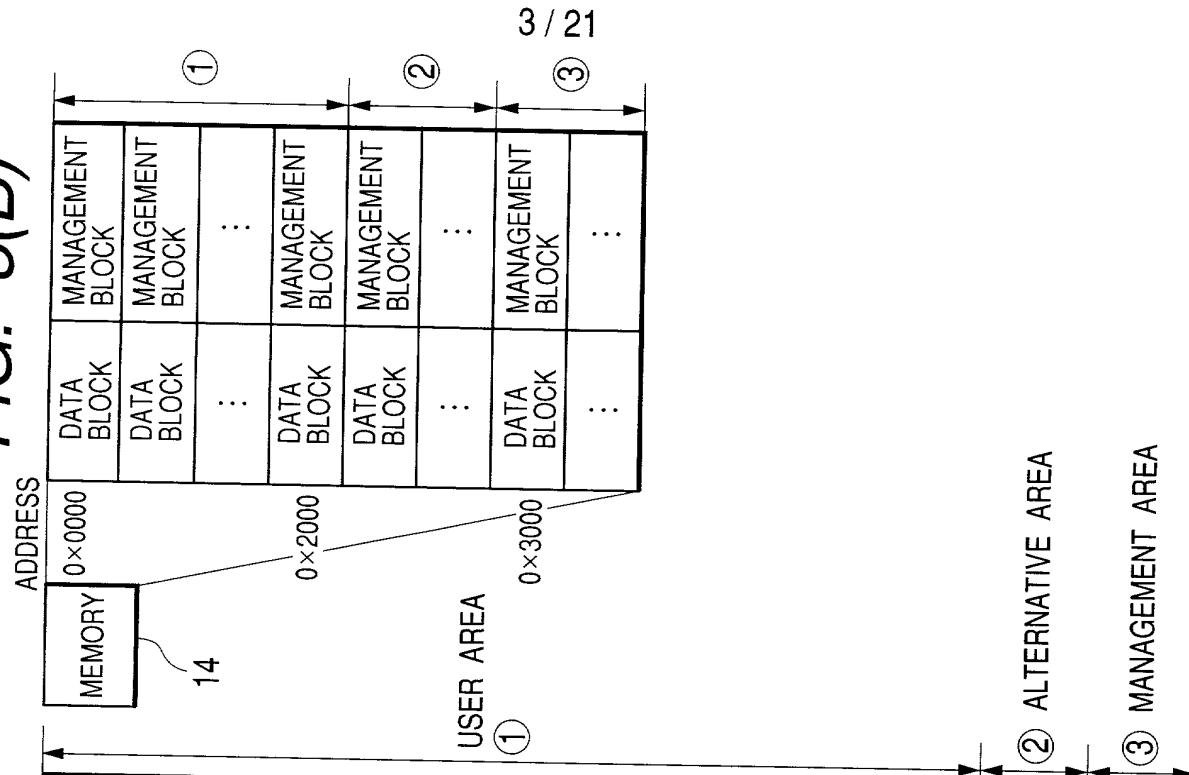


FIG. 4

The diagram illustrates a memory structure organized into three groups (GROUP 0, GROUP 1, GROUP 2) of four pages each. The columns represent the page number (F\_CE\_N), address (F\_D), and ID information. The rows represent the memory pages. Arrows on the left indicate the grouping of pages. A callout box on the right provides operational details for each group.

F_CE_N	ADDRESS (F_D)	ID INFORMATION
1	0×0000	0×01010000000000
⋮	⋮	⋮
1	0×1FFF	0×01010000000000
2	0×0000	0×01010000000000
⋮	⋮	⋮
2	0×0FFF	0×01010000000000
2	0×1000	0×02010000000000
⋮	⋮	⋮
2	0×1FFF	0×02010000000000
3	0×0000	0×04001000000000
⋮	⋮	⋮
4	0×1FFF	0×040021FFF00000

Legend:

- NORMAL READ/WRITE** (Applies to GROUP 0 and GROUP 1)
- READ ONLY** (Applies to GROUP 1)
- MIRRORING (TIME DIFFERENCE)** (Applies to GROUP 2)

FIG. 5

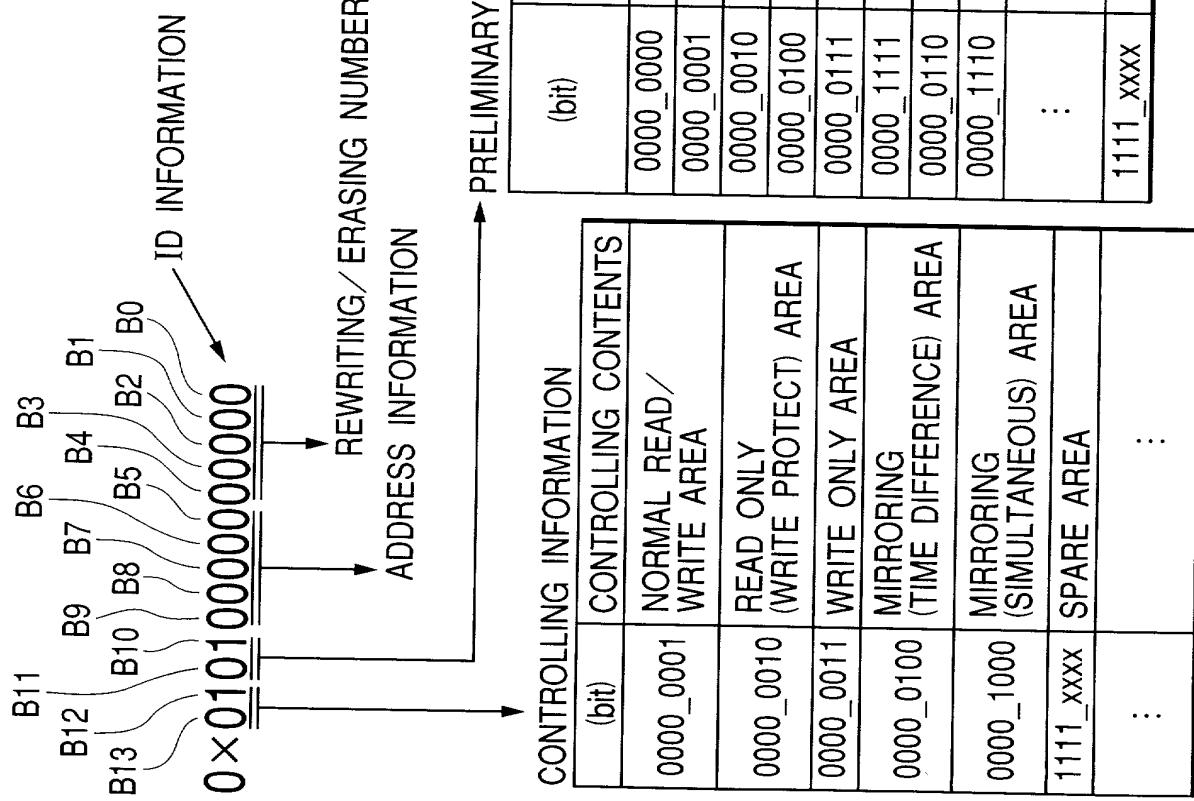


FIG. 6

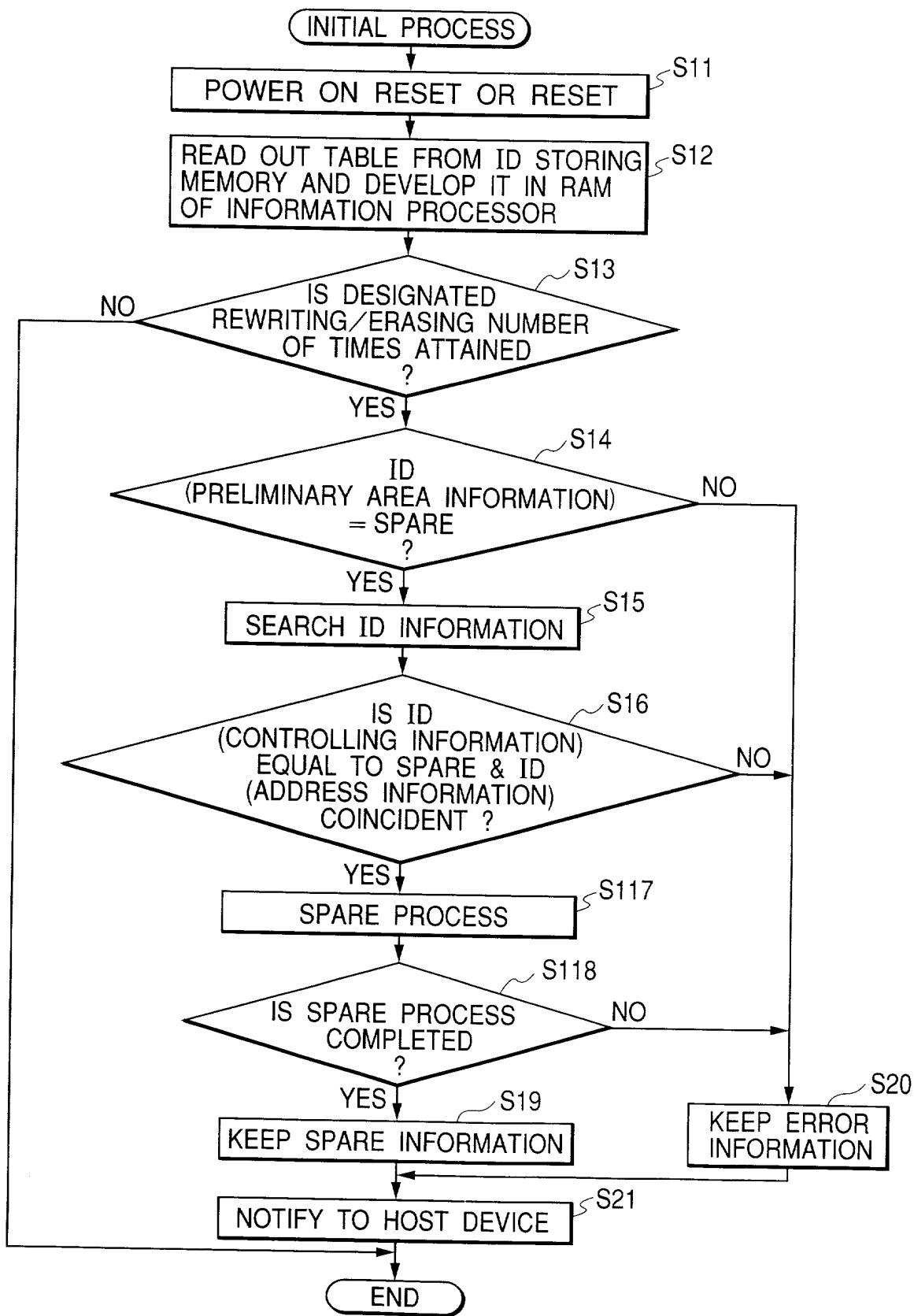


FIG. 7

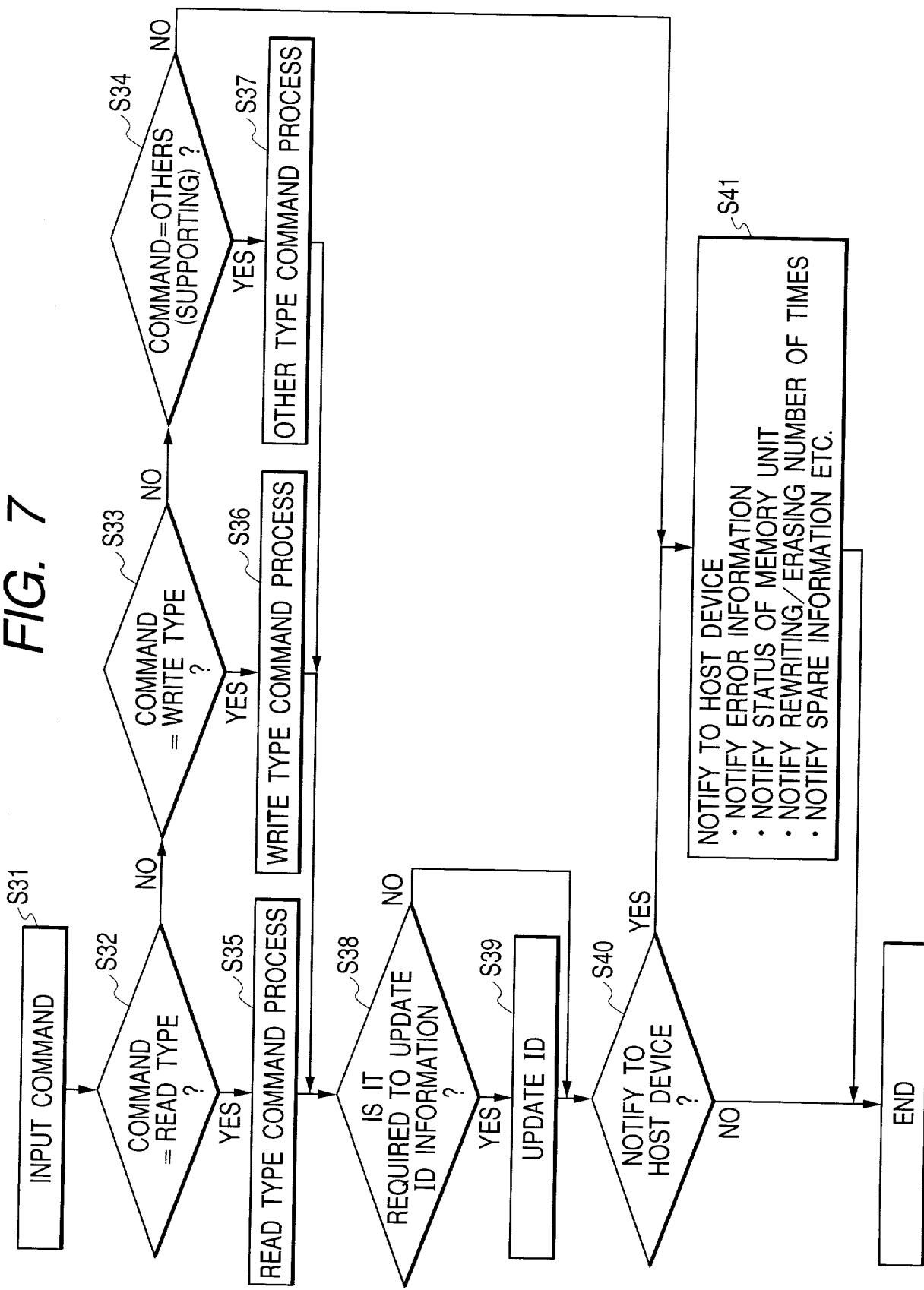
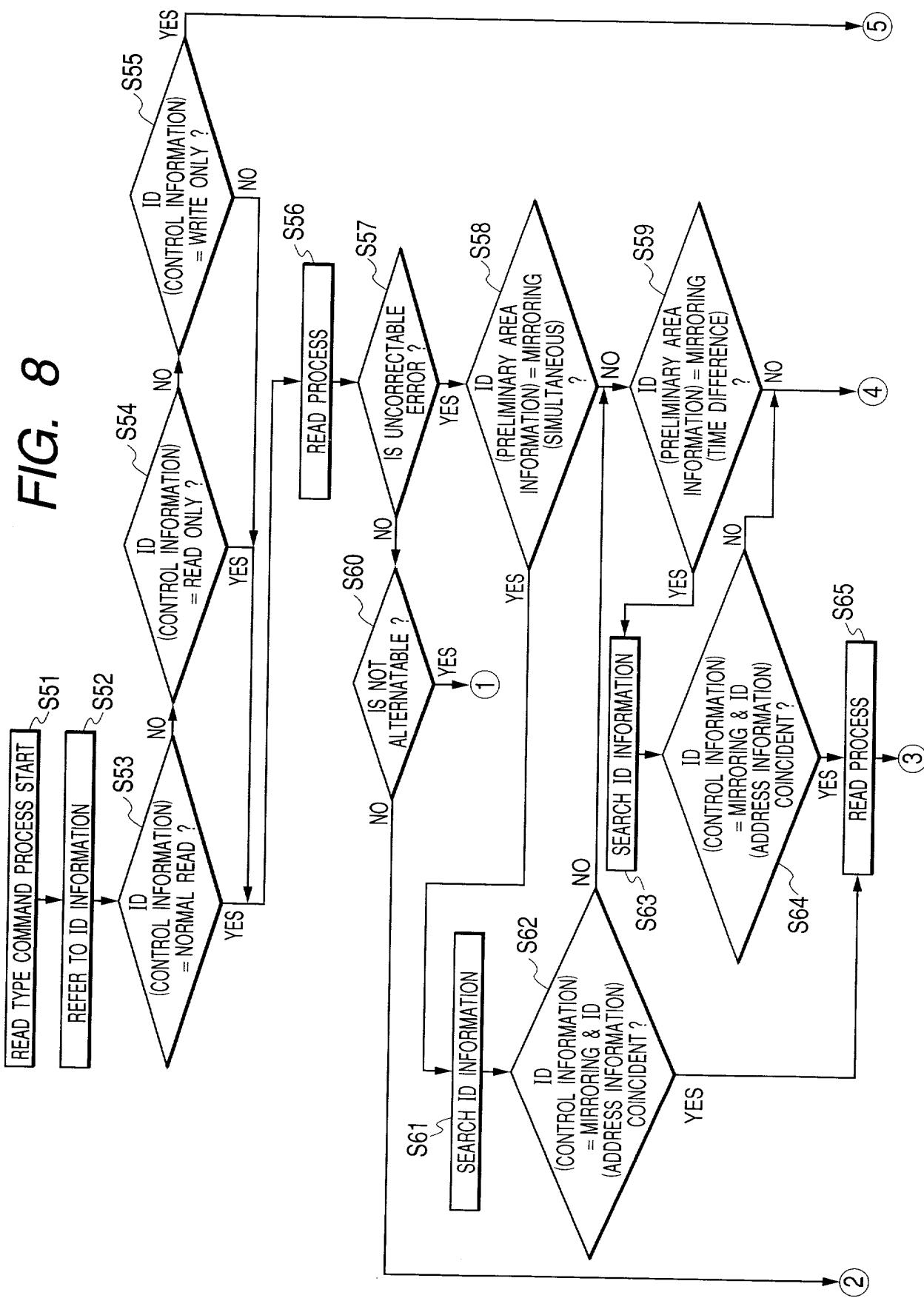


FIG. 8



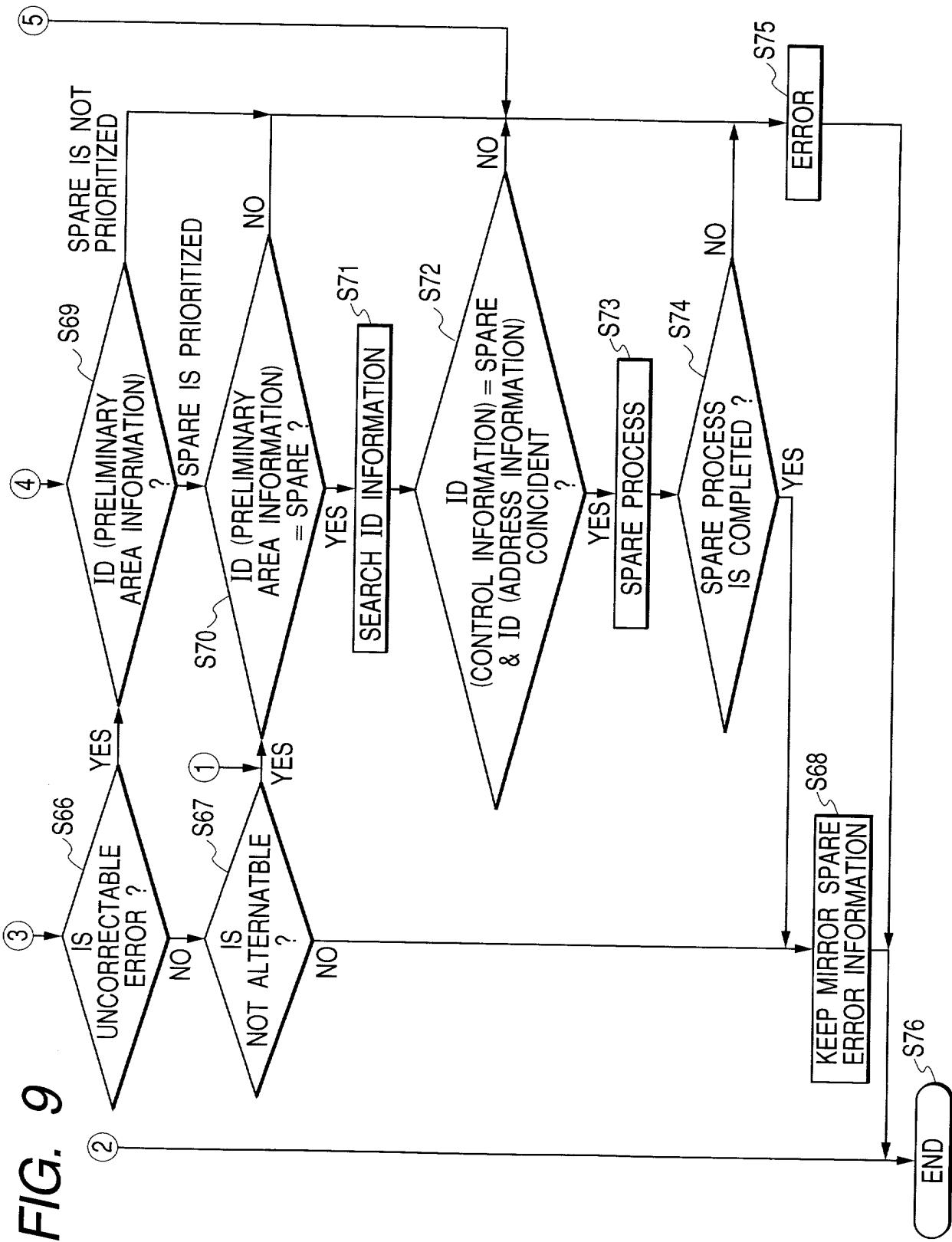


FIG. 10

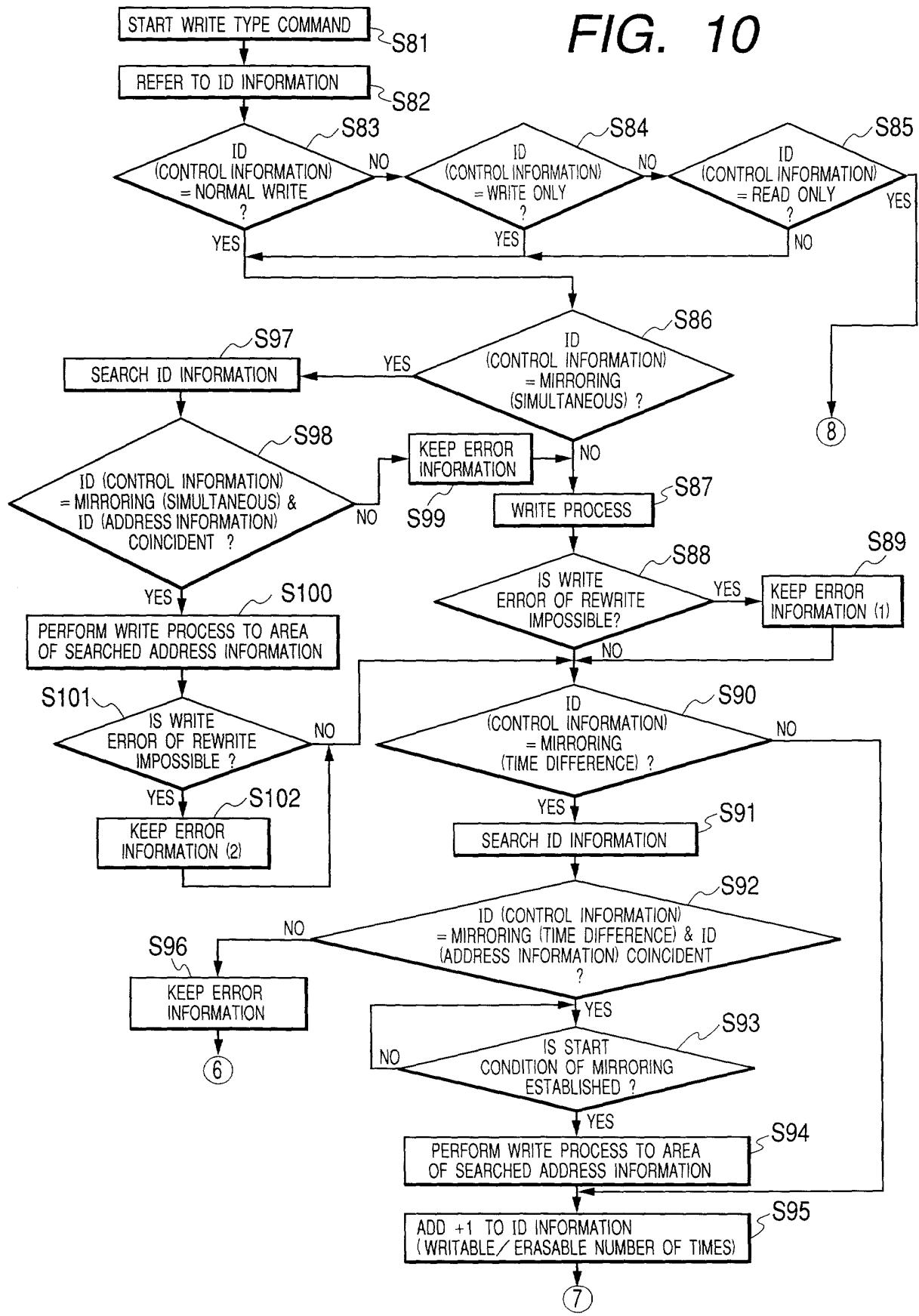


FIG. 11

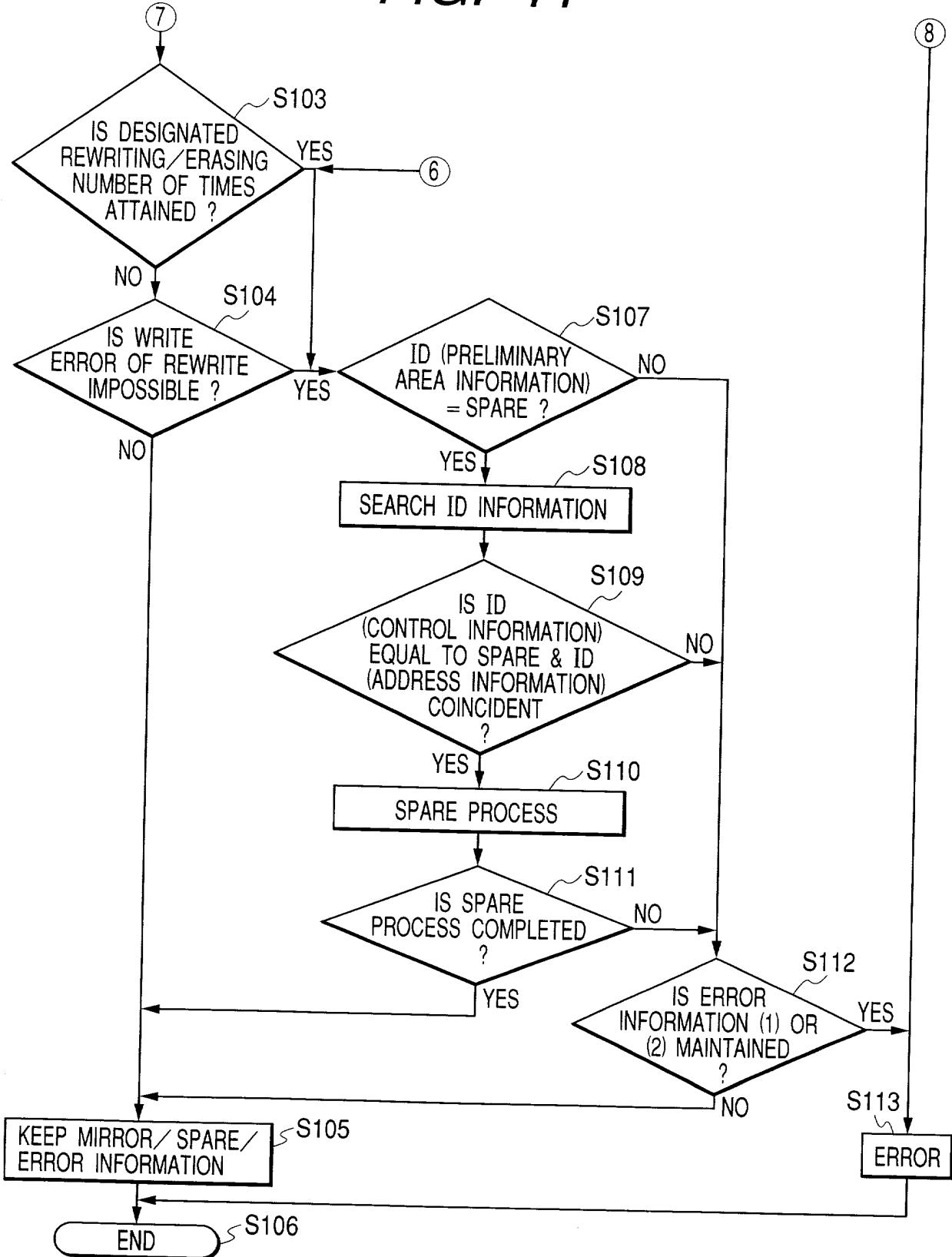


FIG. 12

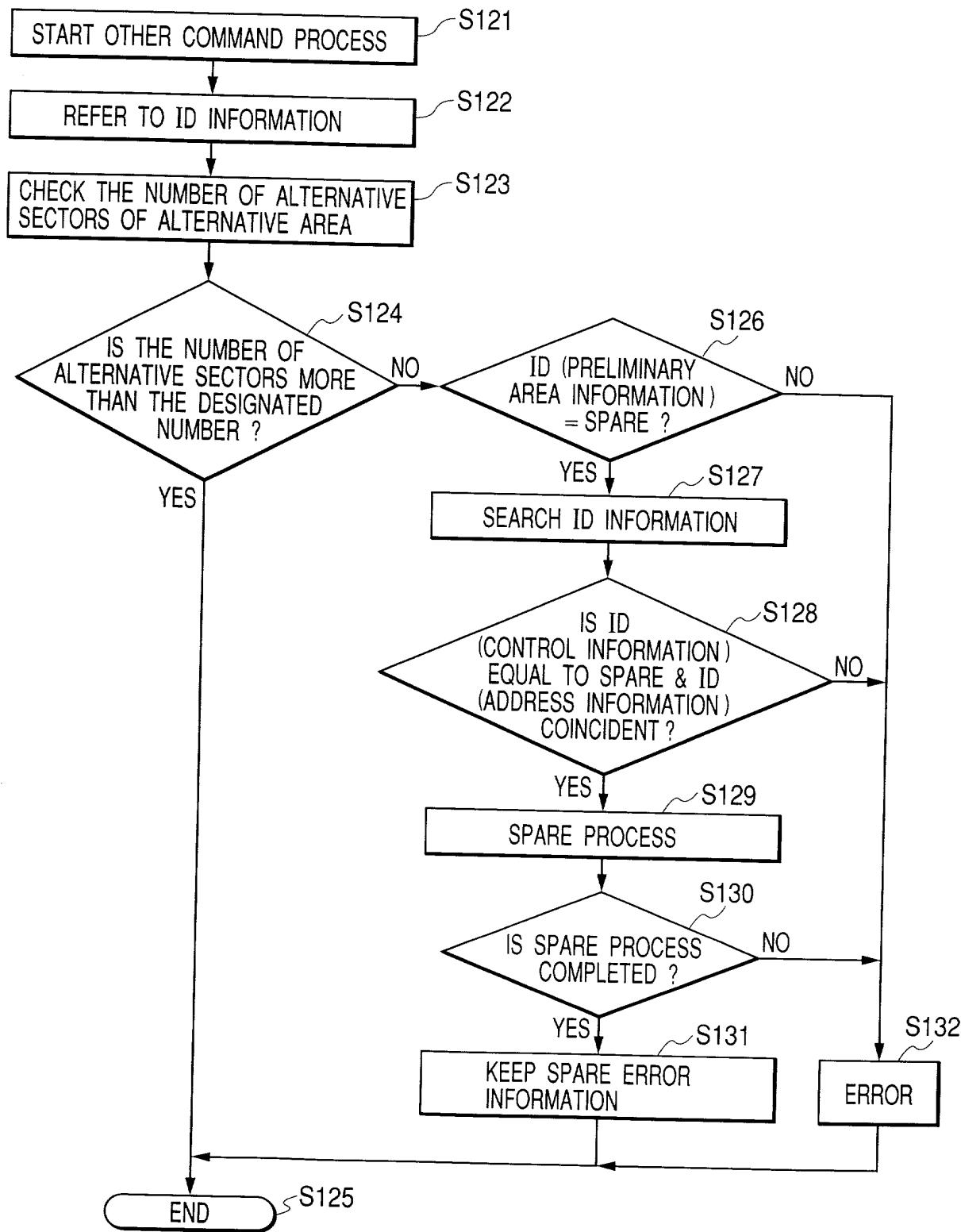


FIG. 13

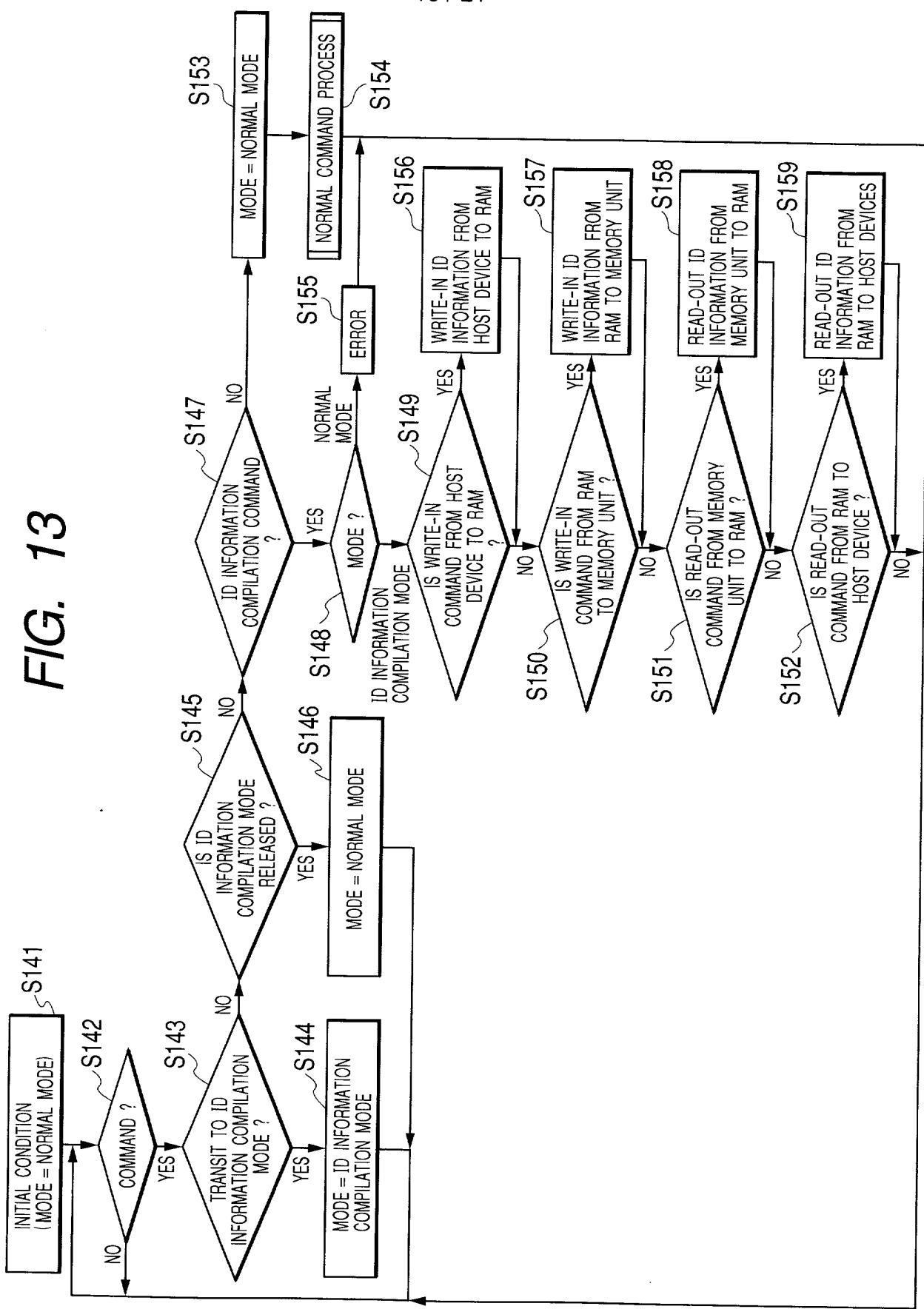


FIG. 14

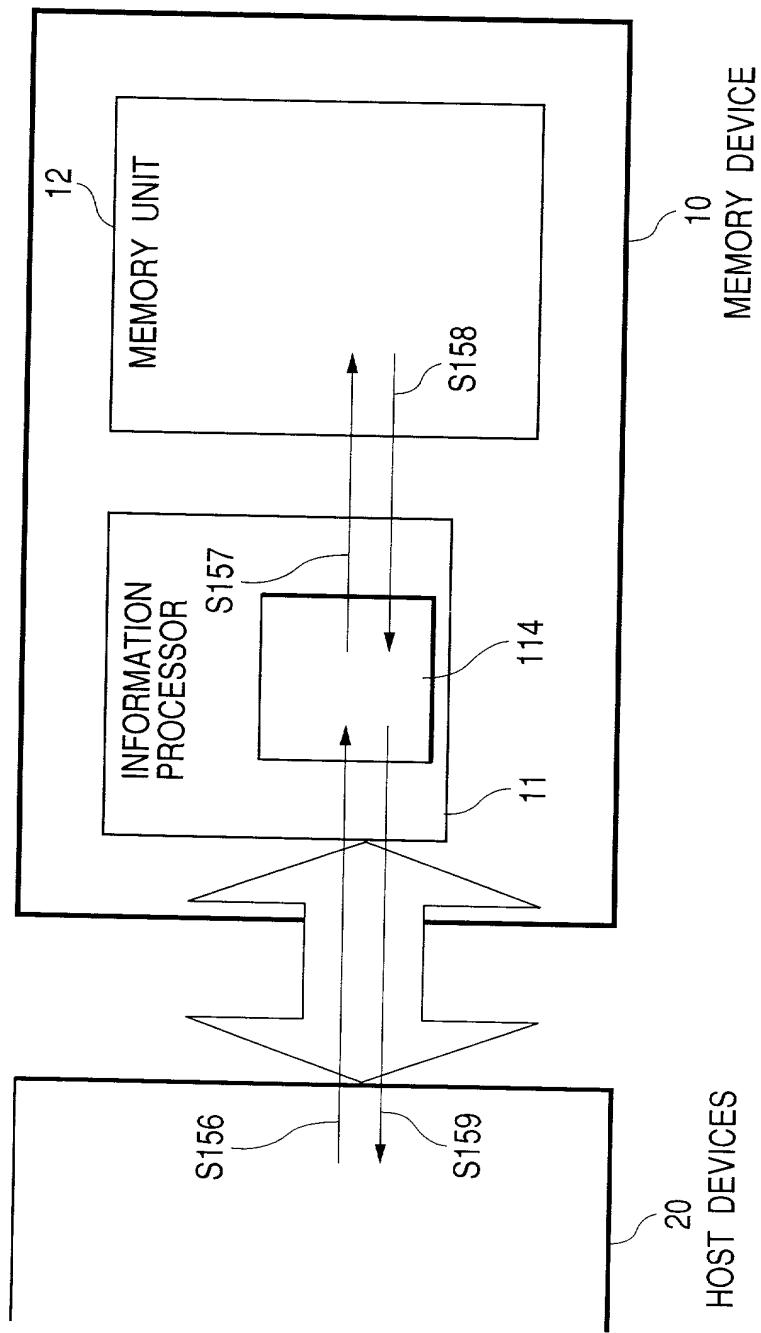


FIG. 15

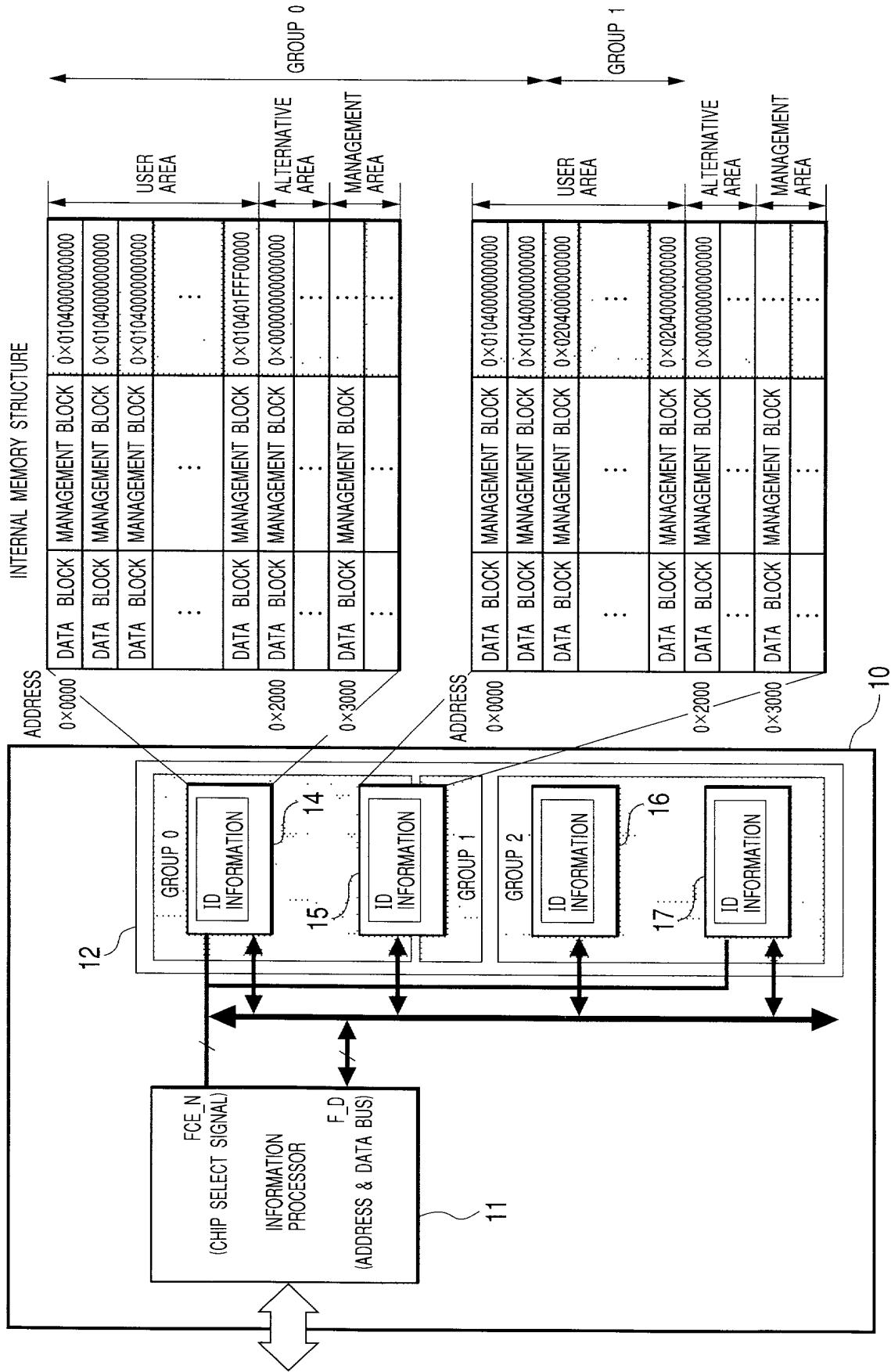


FIG. 16

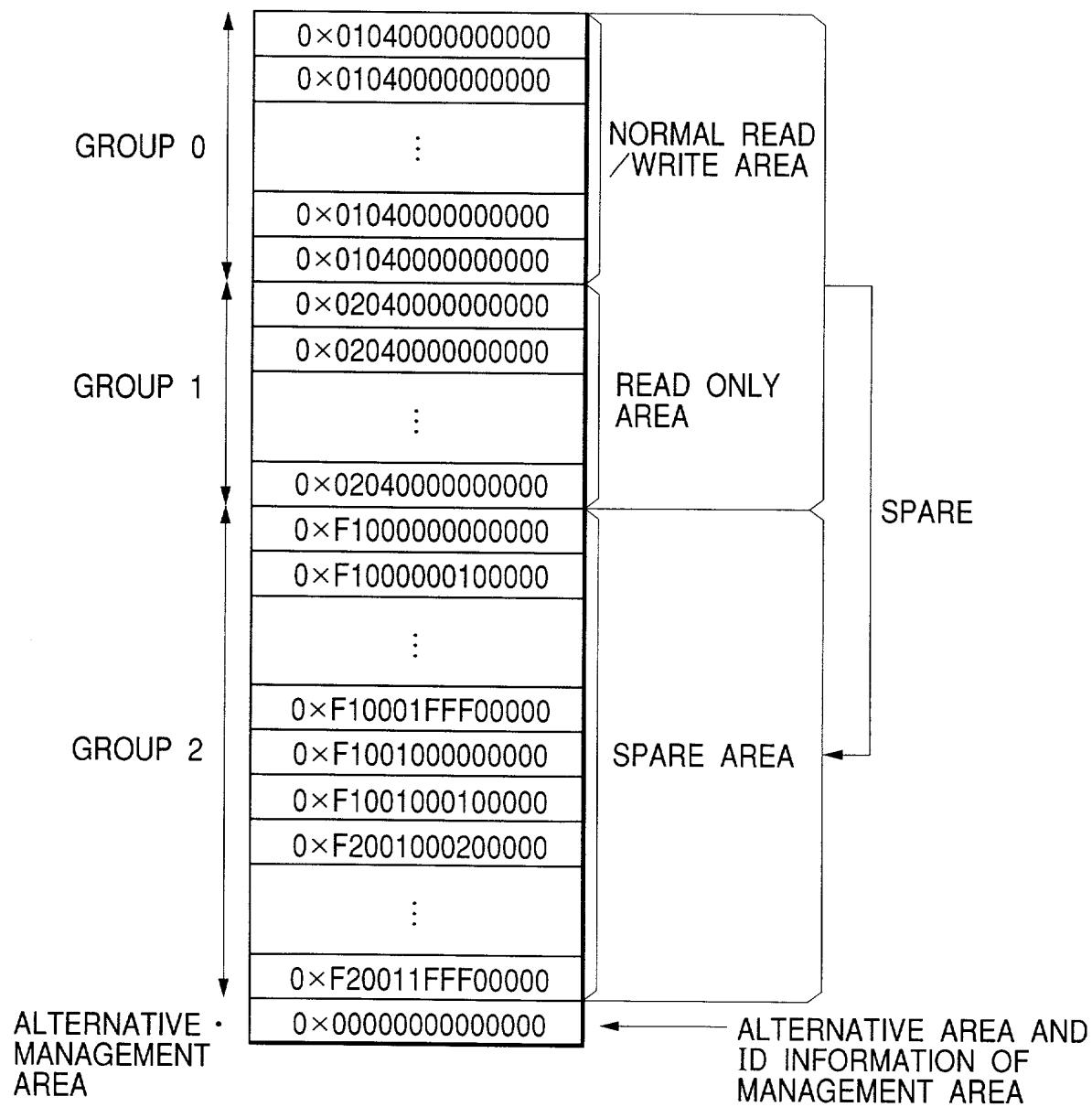


FIG. 17

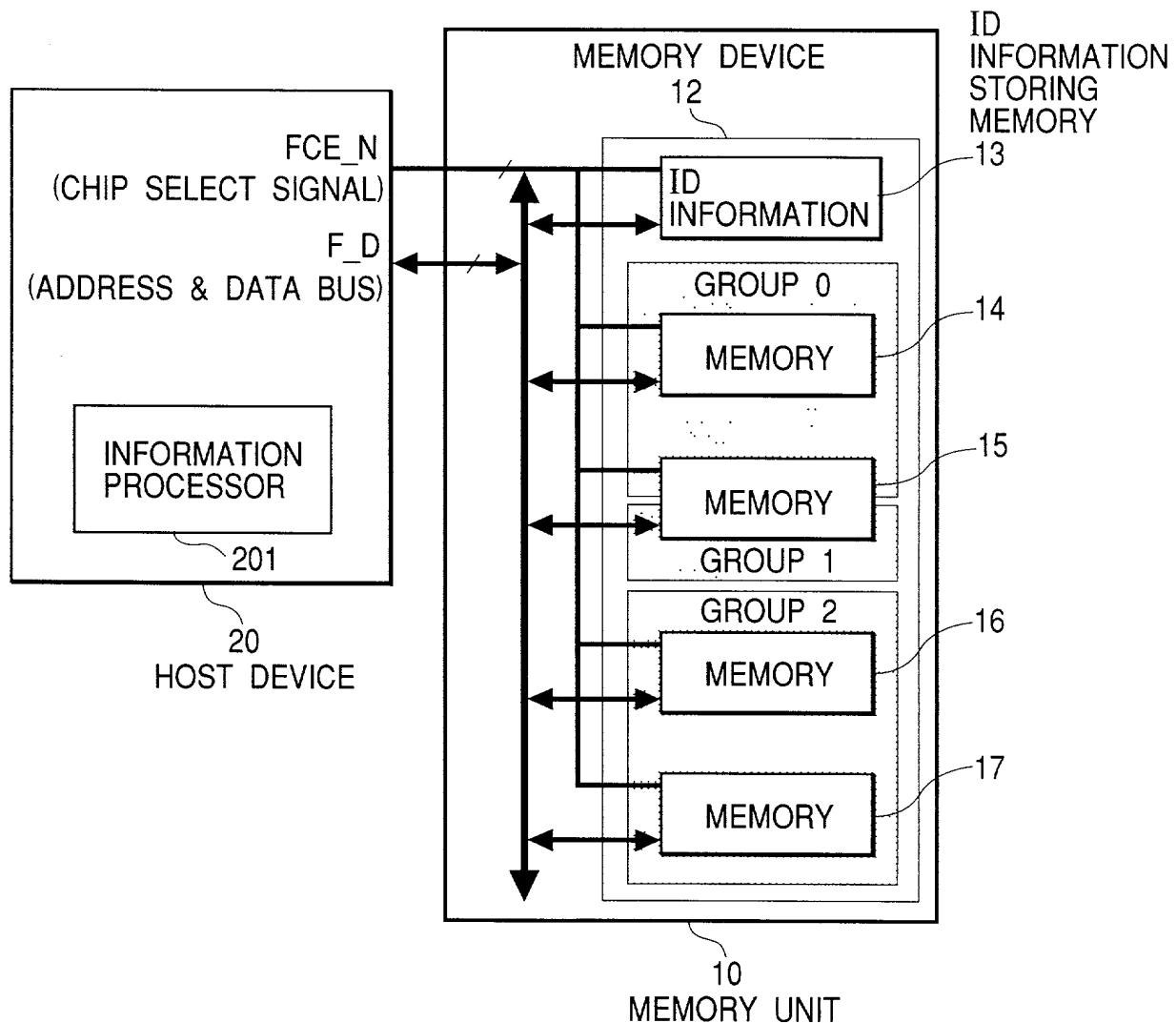


FIG. 18

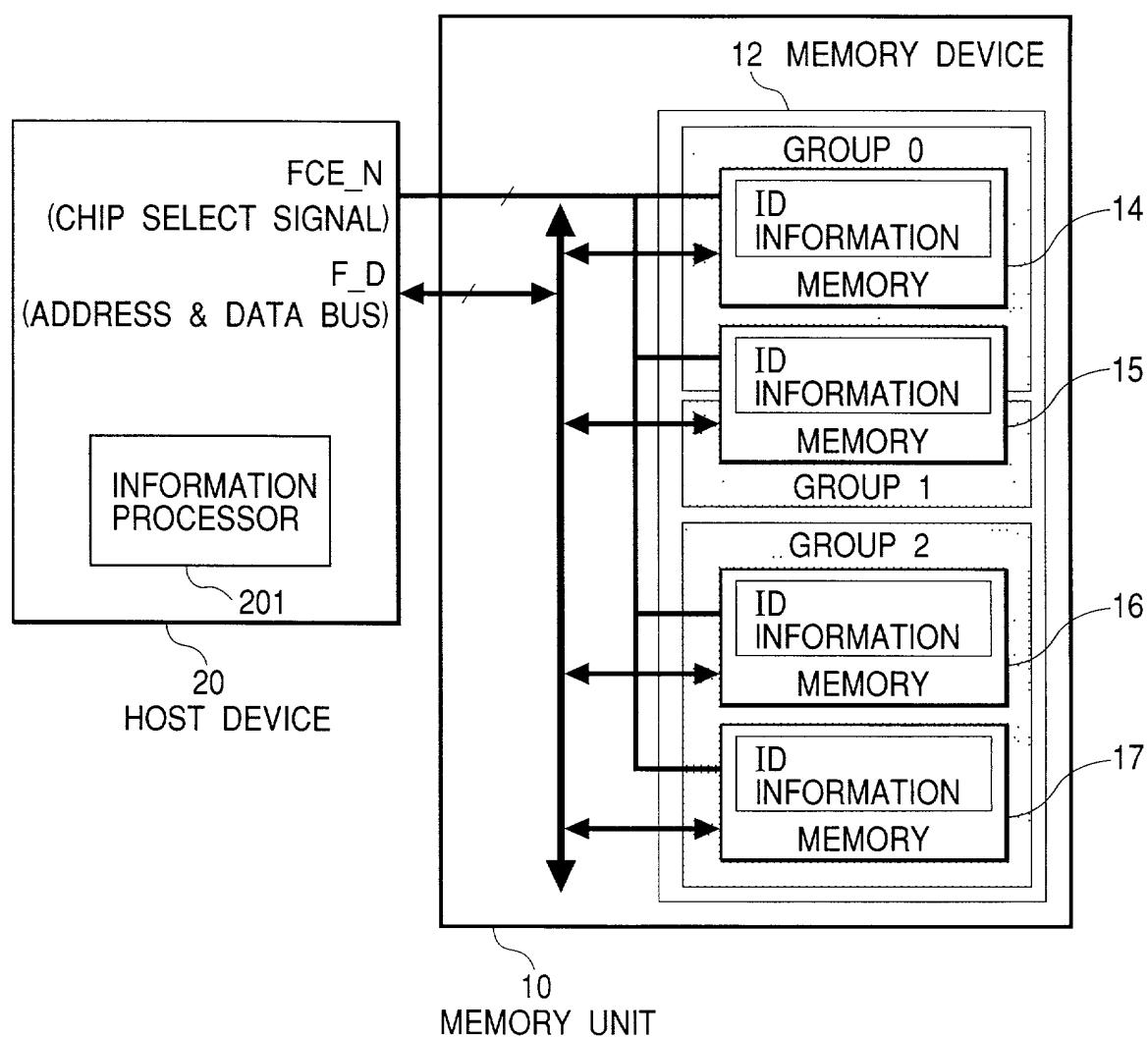


FIG. 19

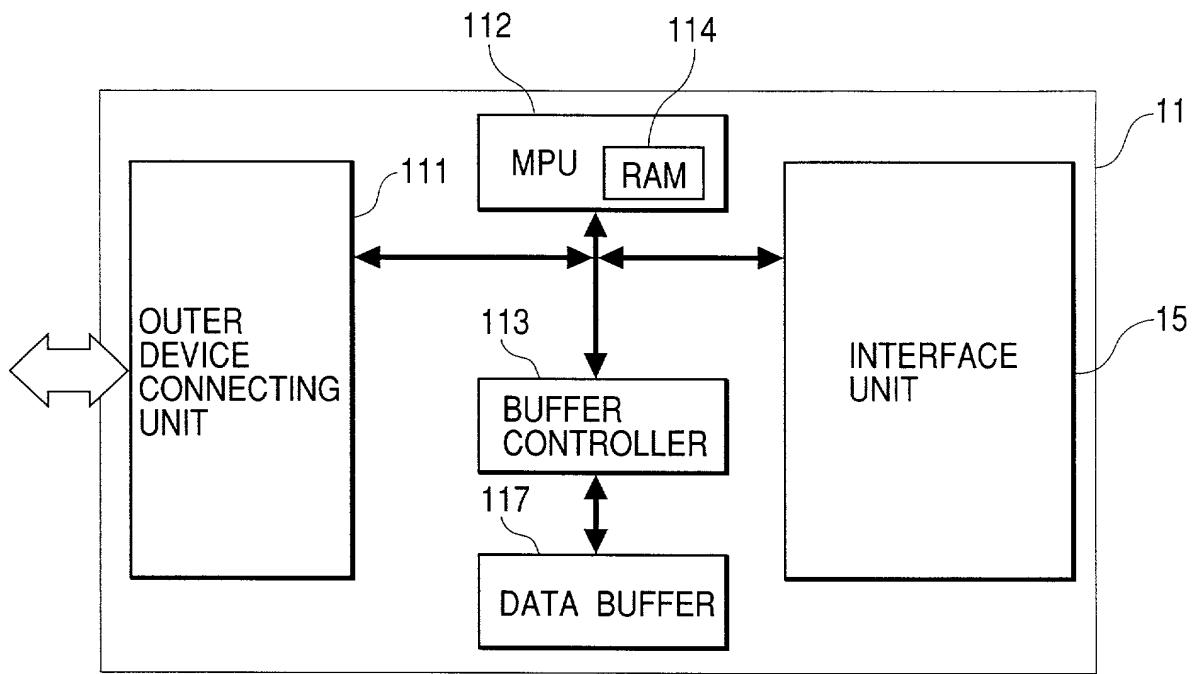


FIG. 20

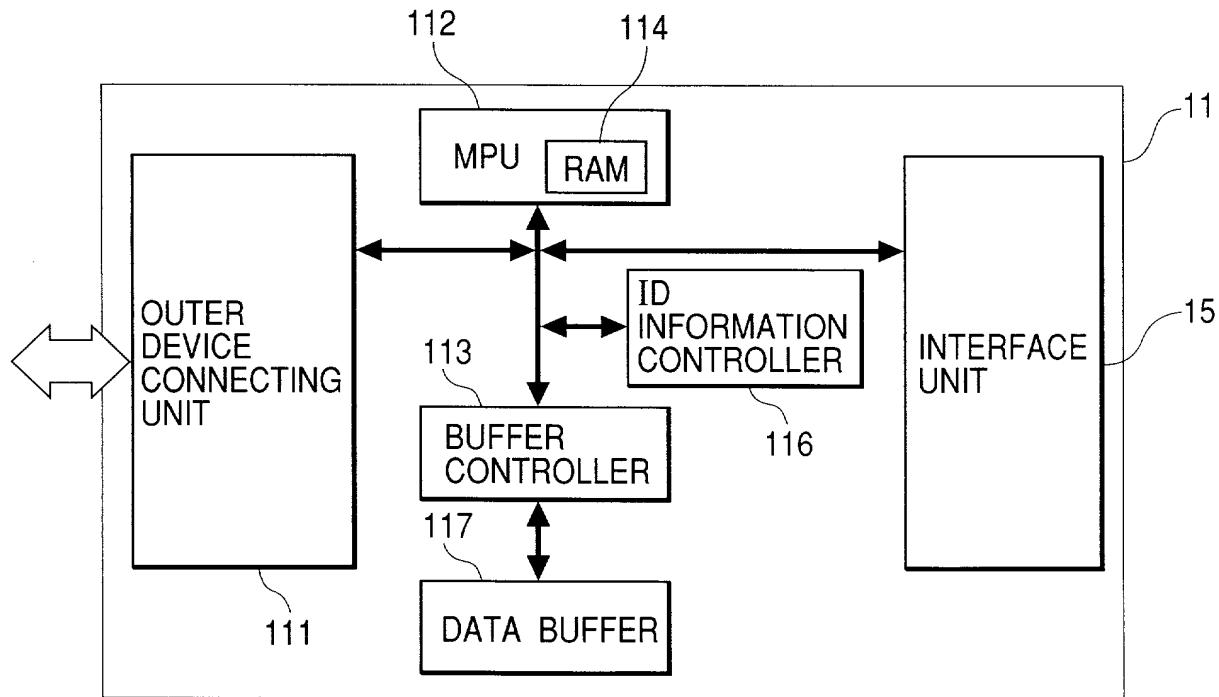


FIG. 21

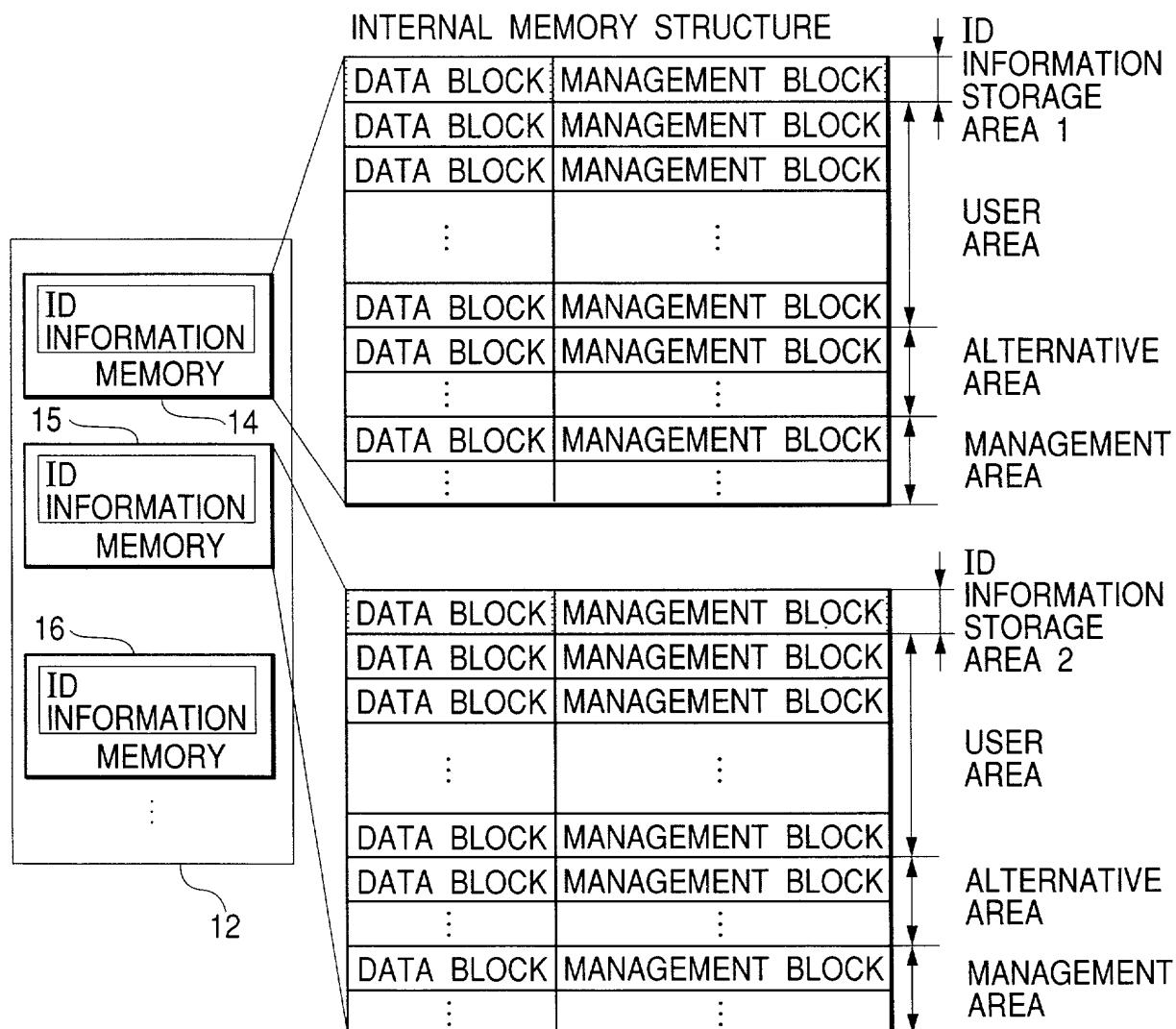


FIG. 22(A)

14,15,16,17

FIG. 22(B)

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